

## REMARKS

As a preliminary matter, Applicants once again respectfully request that the Examiner include U.S. Patent No. 6,870,186 (Park et al.) on a Notice of References Cited Form (Form PTO-892) because this reference is not yet of record, even though it was relied upon for a §102(e) rejection in the March 30, 2007 Office Action.

As an additional preliminary matter, Applicants respectfully request entry of this after-final amendment because no claim amendments that raise new issues are being proposed. More specifically, the only proposed claim amendments are directed to Claim 13, and these proposed amendments merely involve the insertion of four commas (",") to better set apart the phrases "on one surface of a first substrate" and "on one surface of a second substrate" from the remainder of the claim language for ease of understanding this claim. Thus, no words are being added or deleted. Accordingly, since the proposed amendments to Claim 13 are merely amendments of form, Applicants respectfully submit that no new issues are raised by the proposed amendment, and therefore entry of this after-final amendment is proper and such entry is respectfully requested.

Claims 13 and 15 stand rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicants regard as the invention. Applicants respectfully traverse this rejection.

On page 2 of the October 9, 2007 Office Action, the Examiner asserted that independent Claim 13 and associated dependent Claim 15 are indefinite because it is unclear whether the features after the following phrases (as now amended) are part of the claimed

invention: "forming, on one surface of a first substrate, a plurality of light emitting elements" and "forming, on one surface of a second substrate, a circuit."

In response, Applicants respectfully assert that the terms after the phrases at issue are part of the claimed invention, and that the language of the claims is clear under 35 U.S.C §112, second paragraph. More specifically, the first two paragraphs of the body of independent Claim 13 (both starting with the term "forming") clearly recite method steps in which numerous components are formed. Lines 3-8 of Claim 13 recite that the following components are formed on one surface of a *first* substrate: a plurality of light emitting elements and a thin film matrix (where the thin film matrix is further defined to include scan bus lines, data bus lines, thin film transistors and switching elements). All of these elements are recited as being formed in lines 3-8 of Claim 13. Similarly, lines 9-11 of Claim 13 recite that the following component is formed on one surface of a *second* substrate: a circuit (where the circuit is defined as being for controlling the thin film transistors, and the circuit is also defined as being electrically connected to the thin film transistors). Accordingly, Applicants respectfully submit that the language of independent Claim 13 is clear, and therefore Applicants respectfully request the withdrawal of this §112, second paragraph, rejection of independent Claim 13 and associated dependent Claim 15.

Claims 3-7, 13, 15-19, 21 and 22 stand rejected under 35 U.S.C. §102(e) as being anticipated by United States Patent No. 7,142,770 to Yoshida. Applicants respectfully traverse this rejection.

Initially, it appears as though the Examiner made a typographical error regarding the patent number of the Yoshida reference. More specifically, the Examiner indicated that the patent number for the Yoshida reference is "6, 7,233,306." The correct patent number for the Yoshida reference is "7,142,770." Although the reference to *Yoshihara et al.* has patent number 7,233,06, it appears that the Examiner intended to base the rejection on the *Yoshida* reference. If this assumption is incorrect, Applicants respectfully submit that the Examiner should withdraw the October 9, 2007 Office Action, and issue a new Office Action with the correct patent number.

Applicants respectfully request the withdrawal of this §102(e) rejection because the Yoshida reference fails to disclose all of the claimed features of the present invention. More specifically, the Yoshida reference fails to disclose a display device that includes, *inter alia*, "a plurality of light emitting elements," as well as failing to disclose that the thin film transistor matrix, the scan bus lines, and the data bus lines are formed on the same surface of a *first* substrate as the plurality of light emitting elements, and that a circuit for controlling the thin film transistors is formed on a *second* substrate, as defined in independent Claim 3. Applicants also respectfully submit that Yoshida reference fails to disclose the method of making a display device, including similar features, defined in independent Claim 13.

One example of an embodiment defined by Applicants' Claim 3 is shown in Applicants' Figures 6 and 9, which includes first substrate 70 and second substrate 80. As defined in independent Claim 3, the following features are found on one surface of first substrate 70: a plurality of light emitting elements (EL) and a thin film transistor matrix for

controlling the light emitting elements (EL) that includes a plurality of scan bus lines 74, data bus lines 72 and a plurality of thin film transistors (TFTs). As also defined in independent Claim 3, the following feature is found on second substrate 80: a circuit 88 for controlling the TFTs. As mentioned in Amendment E, according to this feature of the present invention, the wiring length can be short between the display substrate (first substrate) and the outside circuit substrate (second substrate) because the substrates are laid on each other. Thus, signal delay of the wiring, and noises of electromagnetic waves and others can be reduced (as described in, for example, the second and the third embodiments of the present invention).

Initially, the device of the Yoshida reference fails to disclose a display device including a plurality of light emitting elements. The Examiner asserted that Figures 2-5 and 15 of the Yoshida reference include a plurality of light emitting elements on substrate 20. However, the device of Figures 2-5 and 15 of Yoshida is a liquid crystal display device that includes a liquid crystal layer 50 (Figure 5) between substrates 10 and 20. A liquid crystal display device does not include light emitting elements, such as the organic or inorganic light emitting elements of the self-emission type display device of the present invention, and the self-emission type display device of the present invention does not include a liquid crystal layer. *See Applicants' Specification*, page 1, line 23 through page 2, line 8. Accordingly, the device of Figures 2-5 and 15 of Yoshida is a different type of display device from that defined in Claim 3, and it does not include the claimed "light emitting elements." A review of Yoshida's Figure 5 confirms that substrate 20 (which the Examiner asserted includes light emitting elements) lacks the claimed "light emitting elements," and instead only includes

light shielding film 2 and counter electrodes 21. Accordingly, as all of the features defined in independent Claim 3 are not disclosed in Yoshida, Applicants respectfully request the withdrawal of this §102(e) rejection of independent Claim 3 and associated dependent Claims 4-7, 16-19, 21 and 22 for at least this reason.

Similarly, independent Claim 13 defines a method for making a display device that includes the step of forming light emitting elements on one surface of a first substrate. Since the light emitting elements are not found in the device of Figures 2-5 and 15 of Yoshida, there is also no disclosure of a step of forming the light emitting elements. Accordingly, Applicants respectfully request the withdrawal of this §102(e) rejection of independent Claim 13 and associated dependent Claim 15 for at least this reason.

Column 1 (lines 18-19) and column 13 (lines 16-23) of Yoshida mention that aspects of Yoshida may be applied to a device using EL light emitting elements. However, there is no disclosure of the location of the light emitting elements in relation to the other components. For example, independent Claims 3 and 13 specifically recite that the light emitting elements are on one surface of a *first* substrate, along with a thin film transistor matrix for controlling the light emitting elements (which matrix includes scan bus lines, data bus lines and thin film transistors), and the circuit for controlling the thin film transistors is on a *second* substrate.

Further, even assuming *arguendo* that light emitting elements could be added to the device of Yoshida, the components that the Examiner identified as corresponding to the claimed thin film transistor matrix (with scan bus lines, data bus lines and thin film

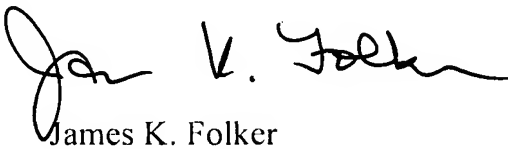
transistors) are located on the same substrate as the component that the Examiner identified as corresponding to the claimed circuit for controlling the thin film transistors, instead of on different substrates, as recited in independent Claims 3 and 13. More specifically, as can be seen in Figures 2 and 5 of the Yoshida reference, components 30, 3a, 6a and 201/202 (which the Examiner asserted correspond, respectively, to the claimed thin film transistor matrix, the scan bus lines, the data bus lines and the circuit for controlling the thin film transistors) are all located on the same substrate (substrate 10). However, independent Claims 3 and 13 require the circuit for controlling the thin film transistors to be located on a different substrate than the other components mentioned above. Accordingly, for this reason also, Applicants respectfully request the withdrawal of this §102(e) rejection of independent Claims 3 and 13 and associated dependent Claims 4-7, 15-19, 21 and 22.

For all of the above reasons, Applicants request reconsideration and allowance of the claimed invention. Should the Examiner be of the opinion that a telephone conference would aid in the prosecution of the application, or that outstanding issues exist, the Examiner is invited to contact the undersigned attorney.

Respectfully submitted,

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